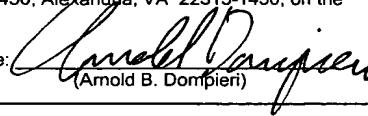


1FD 7/30/54

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Dated: May 28, 2004

Signature:



(Arnold B. Dompien)

Docket No.: SCEI 3.0-058
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Suzuoki et al.

Application No.: 09/816,752

Group Art Unit: 2154

Filed: March 22, 2001

Examiner: J. A. Follansbee

For: PROCESSING MODULES FOR COMPUTER
ARCHITECTURE FOR BROADBAND
NETWORKS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

It is respectfully requested that the references listed on the enclosed form be made of record and considered with respect to the above-referenced U.S. patent application. A copy of each reference is enclosed.

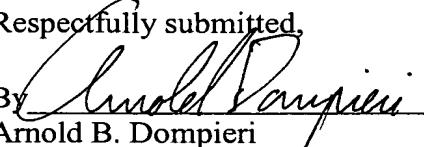
This application is related to U.S. patent application numbers 09/816,004 filed March 22, 2001; 09/815,554 filed March 22, 2001; 09/816,020 (now U.S. patent number 6,526,491); 10/371,322 filed February 21, 2003; and 09/815,558 filed March 22, 2001. The listed references were cited in one or more of these related applications.

Submission of the present Information Disclosure Statement should not be taken as an admission that the cited references are legally available prior art or that the same are pertinent or material.

In the event that any fee is due in connection with the present Information Disclosure Statement, the Commissioner is hereby authorized to charge the same to our Deposit Account No. 12-1095.

Dated: May 28, 2004

Respectfully submitted,

By 

Arnold B. Dompieri

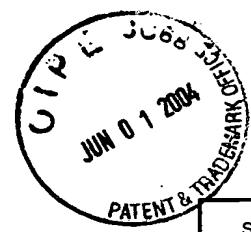
Registration No.: 29,736

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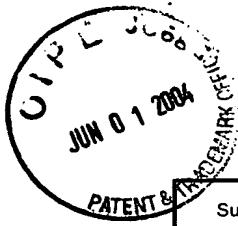


PTO/SB/08a/b (08-03)
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Substitute for form 1449A/B/PTO				Complete if Known	
				Application Number	09/816,752
				Filing Date	March 22, 2001
				First Named Inventor	Masakazu Suzuki
				Art Unit	2154
				Examiner Name	J. A. Follansbee
Sheet	1	of	2	Attorney Docket Number	SCEI 3.0-058

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
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		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)			
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				Art Unit	2154
				Examiner Name	J. A. Follansbee
Sheet	2	of	2	Attorney Docket Number	SCEI 3.0-058

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	BU	JP-2002-032218	01-31-2002	Rikogaku Shinkokai	*
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NON PATENT LITERATURE DOCUMENTS					
Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			²
	CA	MASAKAZU SUZUOKI, Playstation 2 Yur Maikuropurosesssesa Emotion Engine (Microprocessor Emotion Engine for PlayStation 2), bit, Kyoritsu Shuppan Col, Ltd., 01-01-2000, pages 11-18, Vol. 32, No. 1.			
	CB	Baer, et al. "On the Inclusion Properties for Multi-Level Cache Hierarchies," Proceedings of the Annual International Symposium on Computer Architecture, Honolulu, Hawaii, May 30 - June 2, 1988, Washington, IEEE Computer Society Press, pp. 73-80.			
	CC	Mamoru Maekawa, et al., "Distributed Operating System -- That Coming Next to UNIX," 1st Ed., Dec. 25, 1991, Kyoritsu Shuppan Co., Ltd.			✓
	CD	"IBM System/370 Extended Architecture Instruction Manual," 1st Ed., IBM Japan, April 1984, pp. 3-8.			✓
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